

METHOD FOR OPTIMIZING FREQUENCY OF CLOCK SIGNAL AND NETWORK SWITCH USING SAME

ABSTRACT OF THE DISCLOSURE

A method for optimizing frequency of a clock signal is provided for operations of a network switch. The network switch includes a clock signal generator for generating the clock signal, and a plurality of input/output ports for communicating therevia with at least one network node. Firstly, a control signal is asserted to the clock signal generator according to a certain condition of the input/output ports, e.g. the count of the I/O ports in use or the overall data transmission rate of the I/O ports in use. Then, the frequency of the clock signal outputted from the clock signal generator is adjusted in response to the control signal.